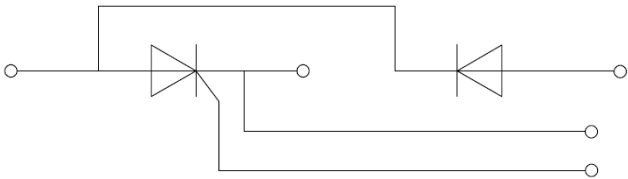




°C		



	°C			
				°C
				°C



	°C					
	°C					
	°C					

°C



impedance. FIG.1: Power dissipation vs. on-state current. FIG.2: Maximum transient thermal

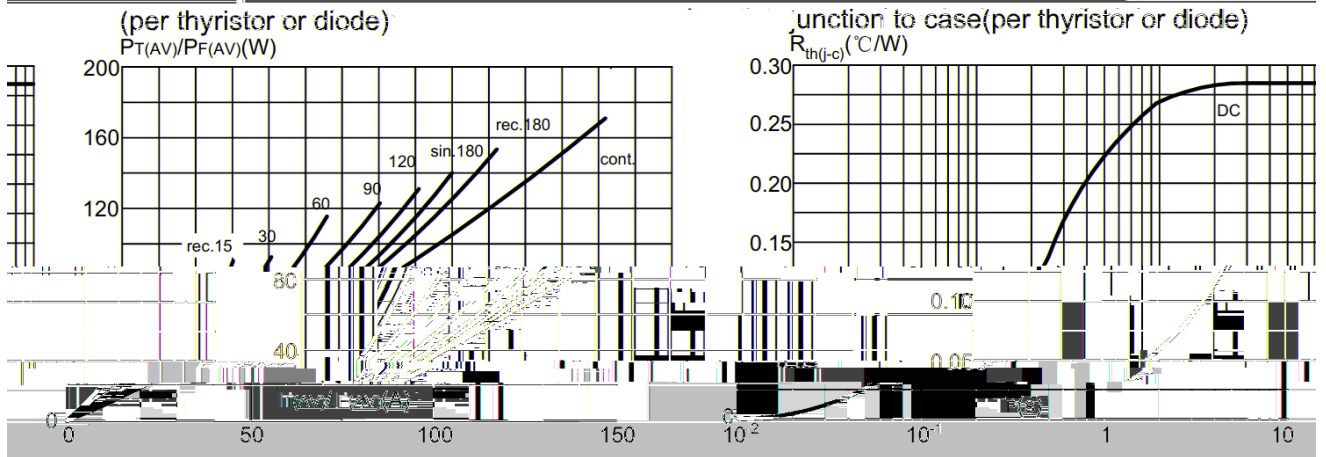


FIG.3: Forward characteristics

FIG.4: Relative variations of gate trigger current, holding current and latching current versus junction temperature

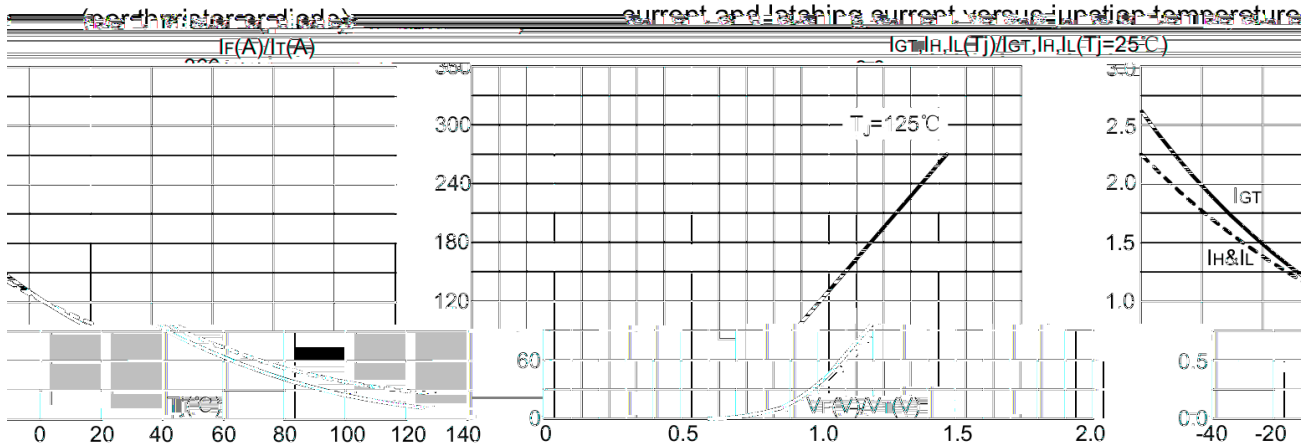


FIG.5: Gate trigger characteristics

FIG.6: Forward current vs. case temperature

