



Half-bridge SCR configuration integrated

In a single package

High-thermal-conductivity DBC insulation

[(c)64H-3(m)-19(s)ex17(ul-4(t)e-10(f)-10(f)-12(d)n] TJ ET Q EMC /Span <</MCI



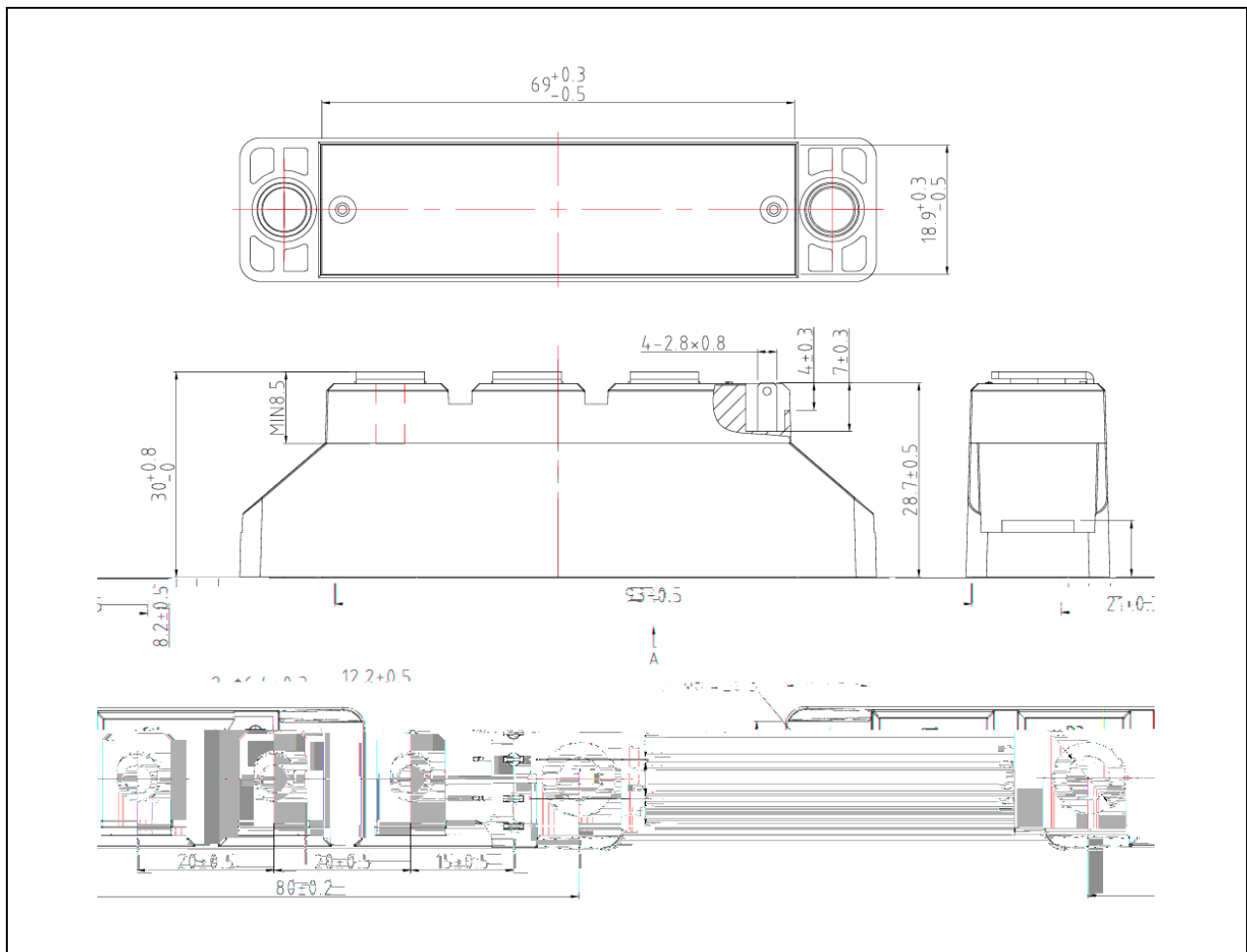
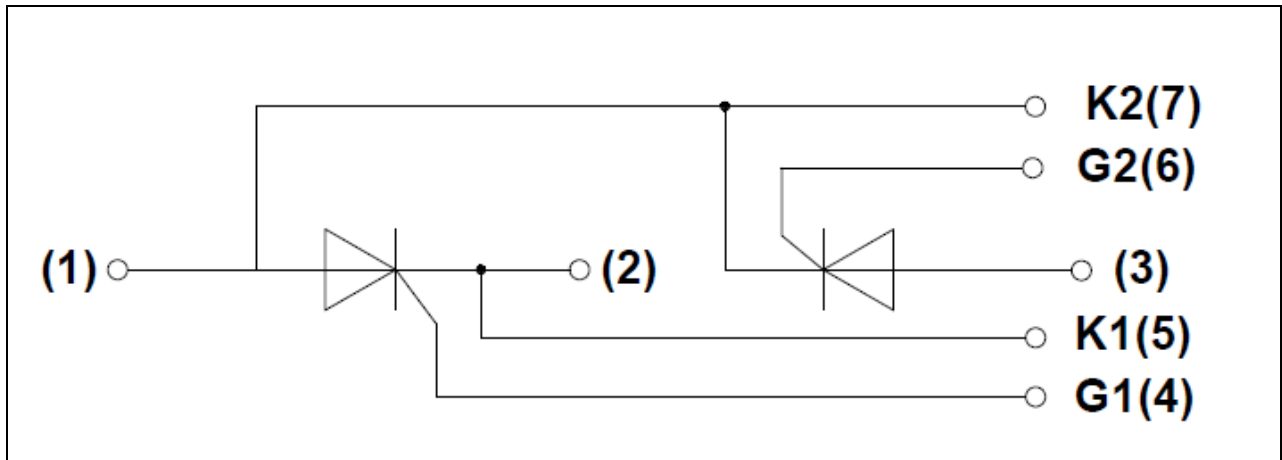
(@ $T_C = 25^\circ\text{C}$ unless otherwise specified)

Peak forward voltage	$I_T=210\text{A}, t_P=380$	V_T			1.80	V
Repetitive peak off-state current	$V_D = V_{\text{DRM}}, T_{vj} = 25^\circ\text{C}$	I_{DRM}			100	A
	$V_D = V_{\text{DRM}}, T_{vj} = 125^\circ\text{C}$				30	mA
Reverse leakage current	$V_R = V_{\text{RRM}}, T_{vj} = 25^\circ\text{C}$	I_{RRM}			100	A
	$V_R = V_{\text{RRM}}, T_{vj} = 125^\circ\text{C}$				30	mA
Threshold voltage	For power loss calculation only $T_{vj} = 125^\circ\text{C}$	V_{TO}			0.92	V
Dynamic resistance	$T_{vj} = 125^\circ\text{C}$	r_T			3.3	
Triggering gate current	$V_D=12\text{V } R_L$	I_{GT}	20		120	mA
Holding current	$I_T=1\text{A}$	I_H			250	mA
Latching current	$I_G=1.2 I_{\text{GT}}$	I_L			300	mA
Critical rate of rise of voltage	$V_D=2/3V_{\text{DRM}} T_{vj}=125^\circ\text{C}$ Gate Open	dv/dt	1000			

Triggering gate voltage

$V_D=12\text{V } R$







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