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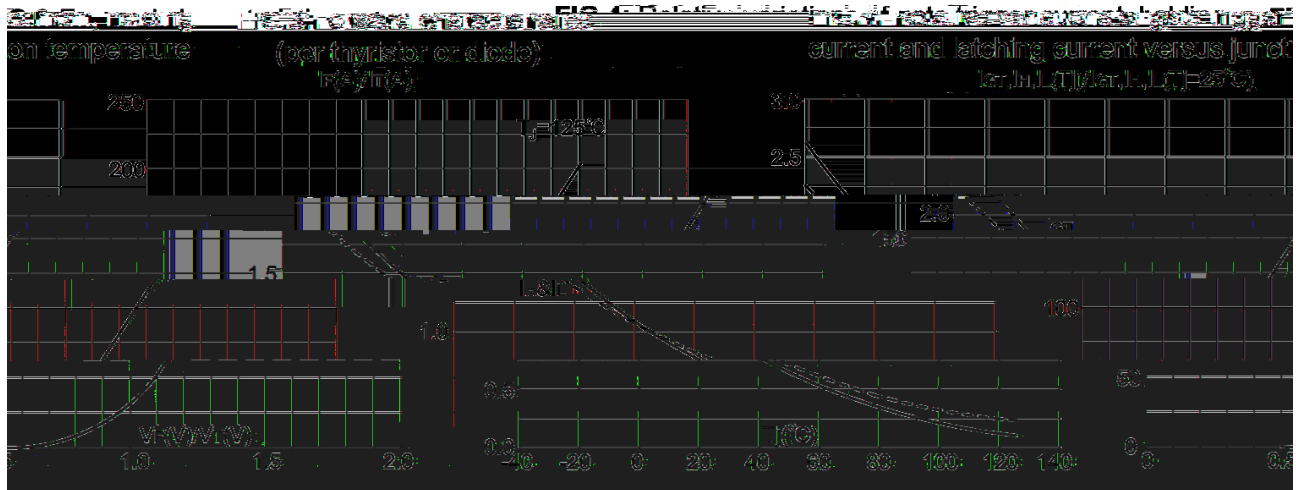
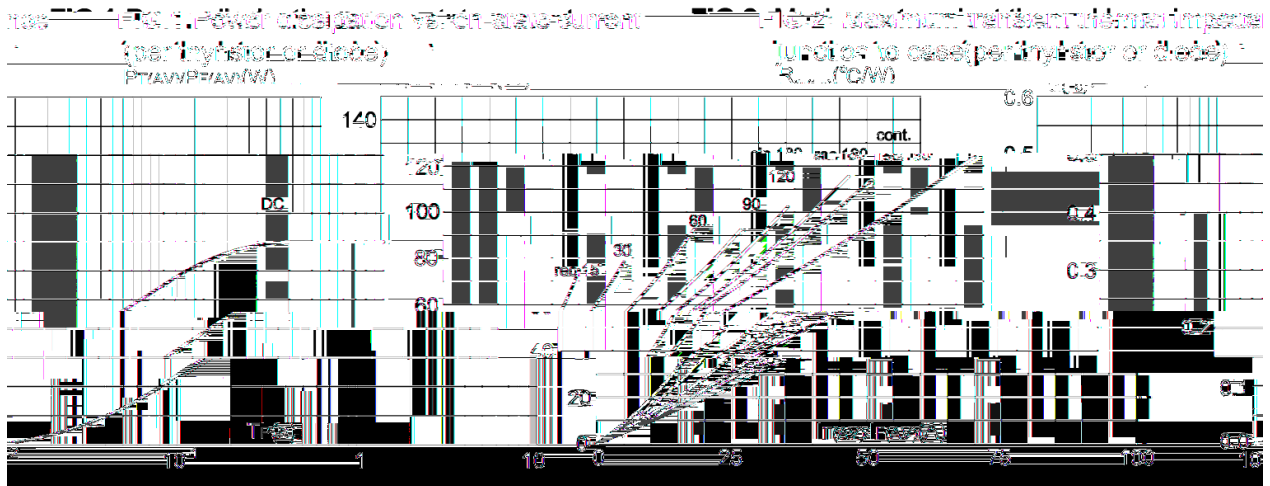


FIG.5 Gate trigger characteristics

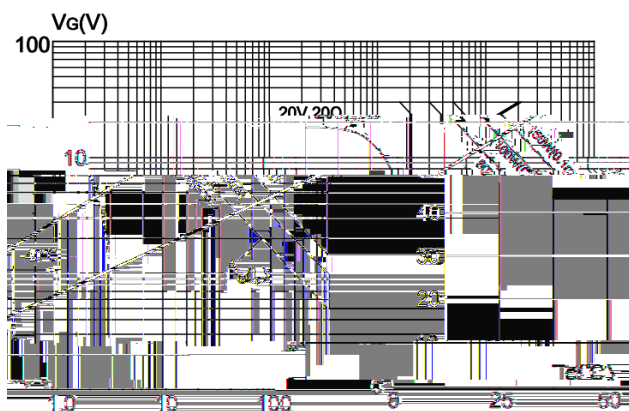
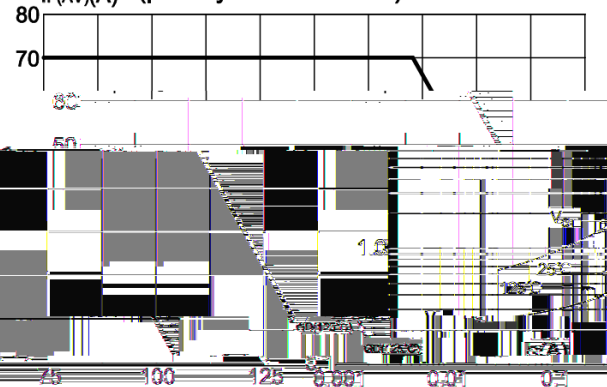
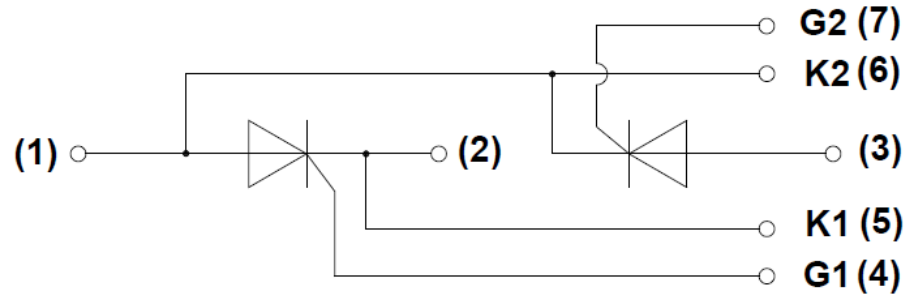


FIG.6: Forward current vs. case temperature
 $I_{F(AV)}$ (A) (per thyristor or diode)

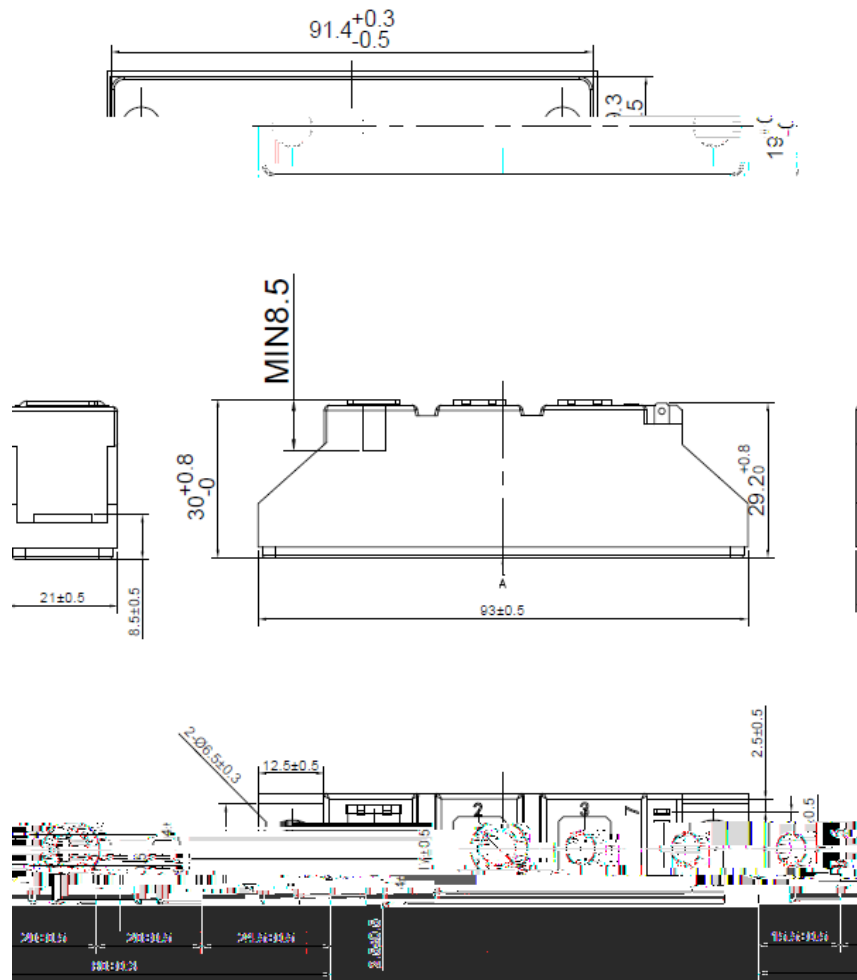


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