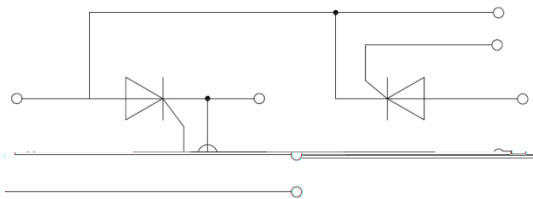




°C		



	°C			
				°C
				°C



	°C					
	°C					
	°C					
	°C					
	°C					
	°C					



FIG.1: Power dissipation vs. on-state current

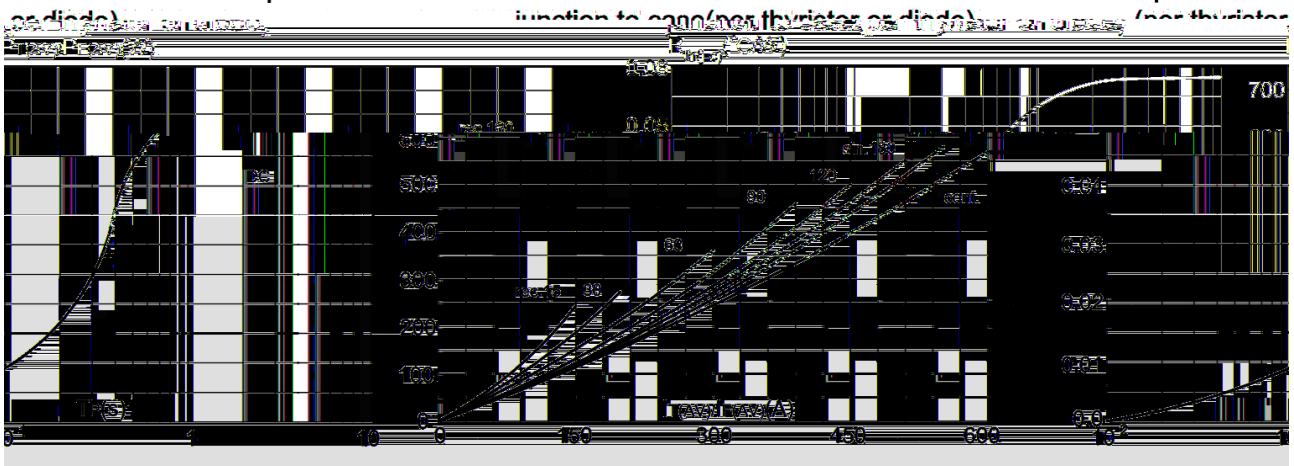


FIG.2: Maximum transient thermal impedance

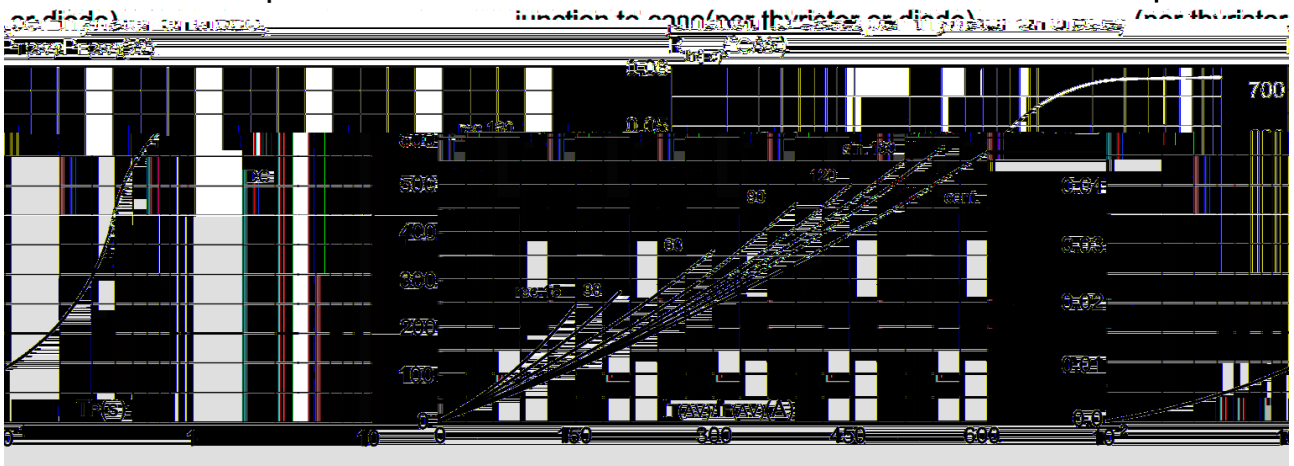


FIG.3: Peak-state current

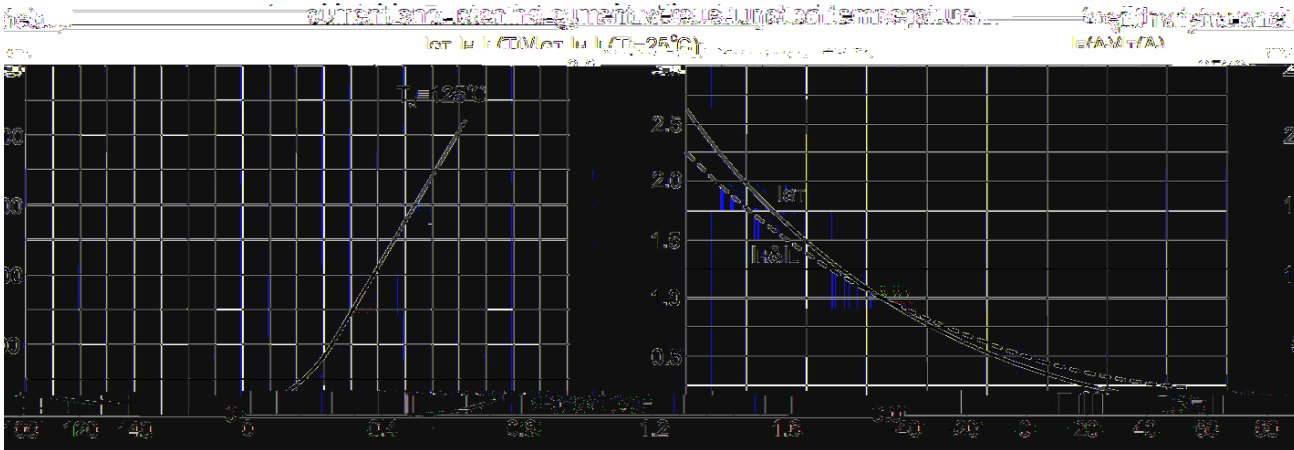


FIG.4: Relative junction of gate trigger current holding

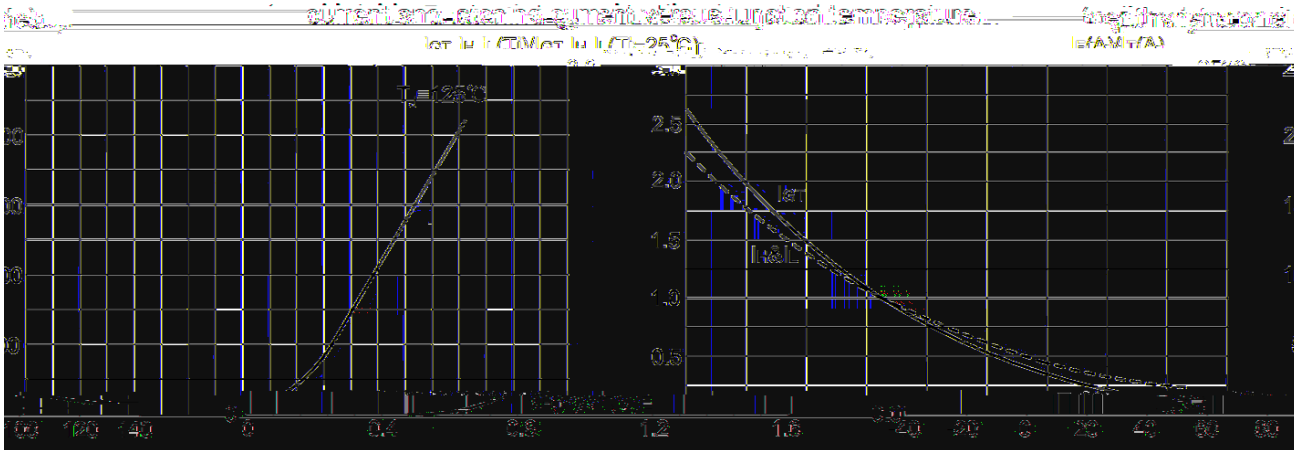


FIG.5: Forward current vs. case temperature

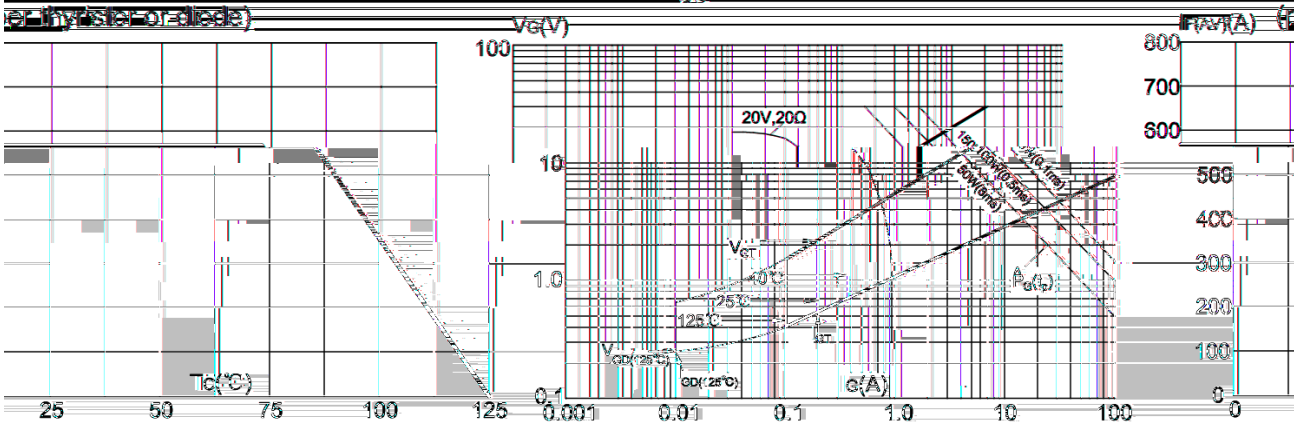


FIG.6: Forward current vs. case temperature

