FIG.1:Power dissipation vs. on-state current

FIG.2: Maximum transient thermal impedance

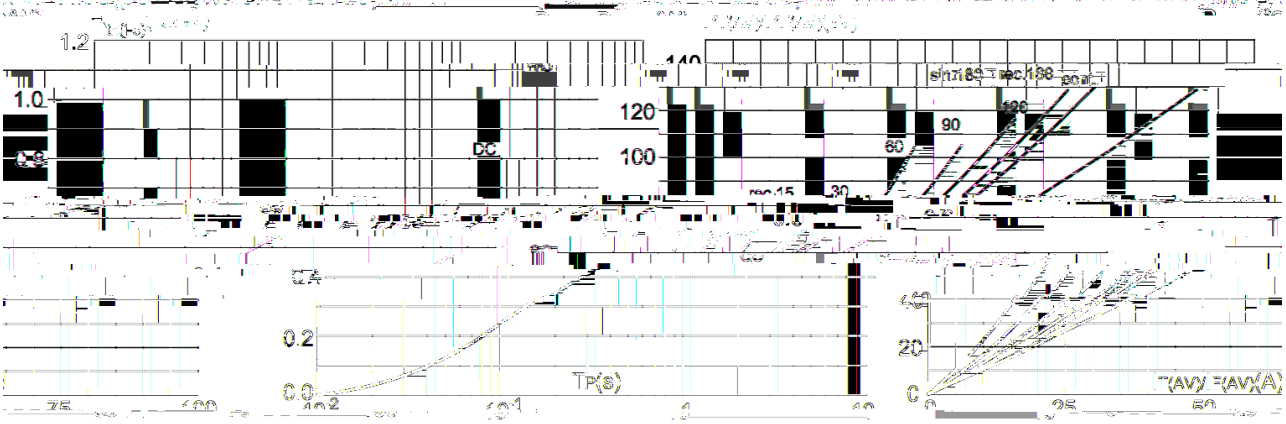


FIG.3:Forward characteristics (per thyristor or diode)

FIG.4: Relative variations of gate trigger current, holding current and latching current versus junction temperature

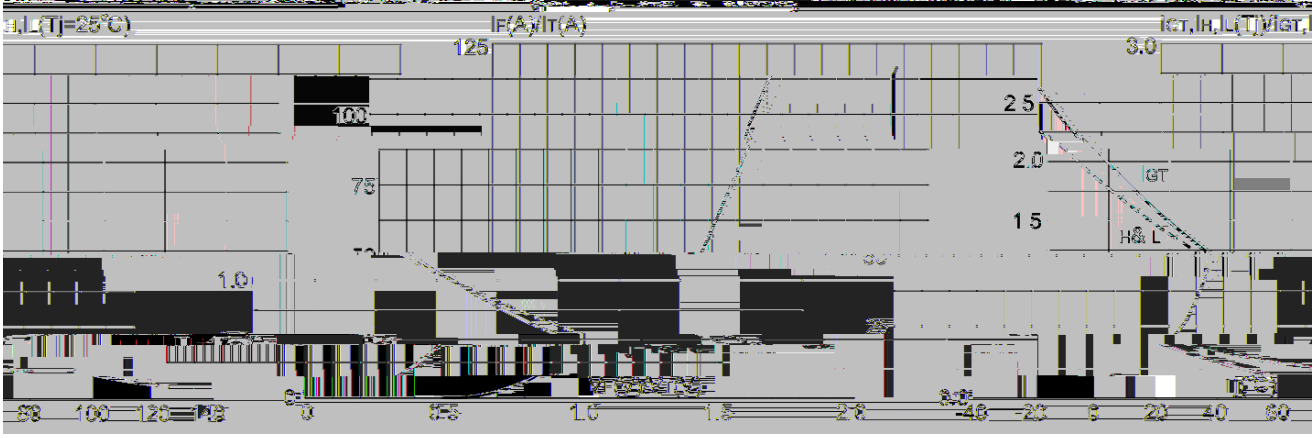


FIG.5 Gate trigger characteristics

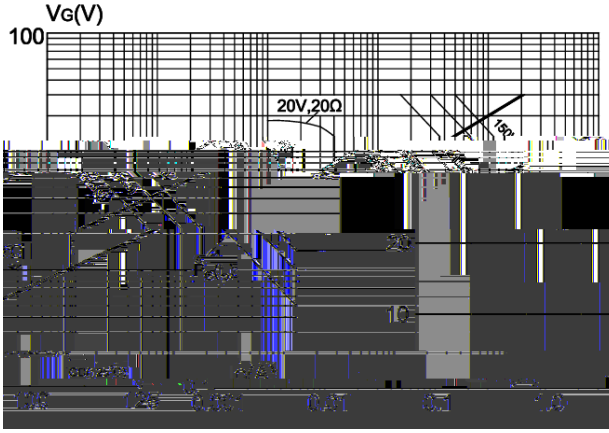
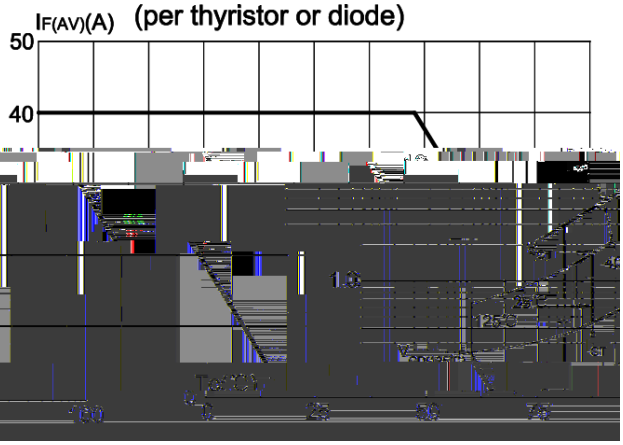
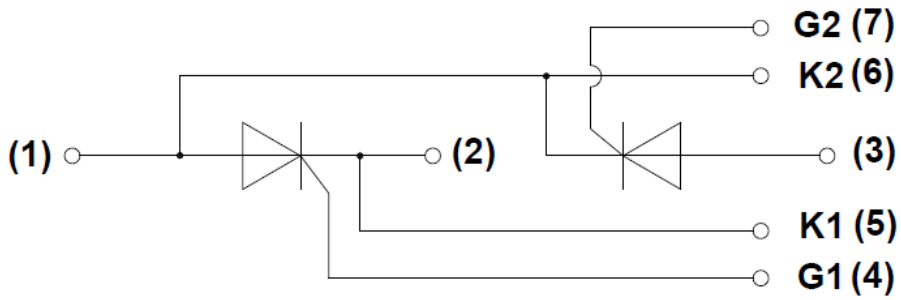
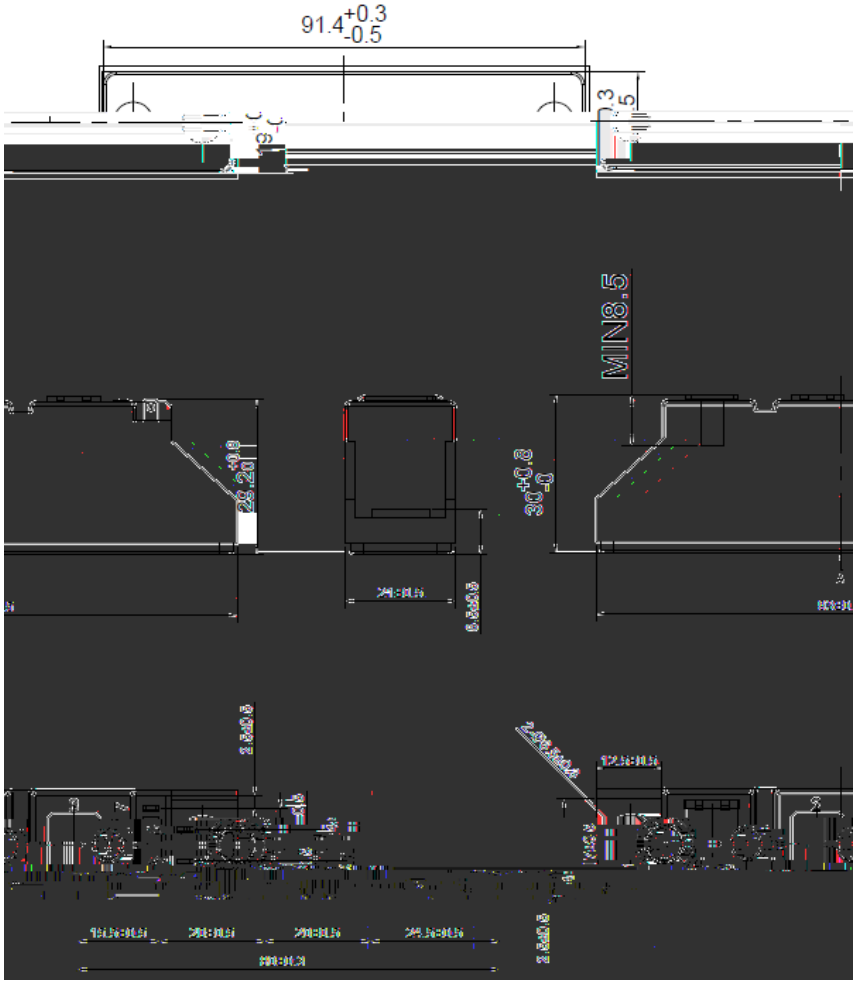


FIG.6: Forward current vs. case temperature (per thyristor or diode)




G2 (7)
K2 (6)
(1)
(2)
(3)
K1 (5)
G1 (4)


91.4+0.3 -0.5
MIN8.5