



(@ $T_C = 25^\circ\text{C}$ unless otherwise specified)

J						
			G (	(	G (	
Peak forward voltage	$I_T=300\text{A}$, $t_P=380\mu\text{s}$	V_T			1.85	V
Repetitive peak off-state current	$V_D = V_{\text{DRM}}$, $T_{vj} = 25^\circ\text{C}$	I_{DRM}			100	μA
	$V_D = V_{\text{DRM}}$, $T_{vj} = 125^\circ\text{C}$				20	mA
Reverse leakage current	$V_R = V_{\text{RRM}}$, $T_{vj} = 25^\circ\text{C}$	I_{RRM}			100	μA
	$V_R = V_{\text{RRM}}$, $T_{vj} = 125^\circ\text{C}$				20	mA
Threshold voltage	For power loss calculation only $T_{vj} = 125^\circ\text{C}$,	V_{TO}			0.9	V
Dynamic resistance	$T_{vj} = 125^\circ\text{C}$,	r_T			3.5	$\text{m}\Omega$
Triggering gate current	$V_D=12\text{V}$ $R_L=30\Omega$	I_{GT}	20		120	mA
Holding current	$I_T=1\text{A}$	I_H			250	mA
Latching current	$I_G=1.2 I_{\text{GT}}$	I_L			350	mA
Critical rate of rise of voltage	$V_D=2/3V_{\text{DRM}}$ $T_{vj}=125^\circ\text{C}$ Gate Open	dv/dt	1000			$\text{V}/\mu\text{s}$
Triggering gate voltage	$V_D=12\text{V}$ $R_L=30\Omega$	V_{GT}			1.5	V
Non triggering gate voltage	$V_D=V_{\text{DRM}}$ $T_{vj}=125^\circ\text{C}$	V_{GD}	0.2			V

(@ $T_C = 25^\circ\text{C}$ unless otherwise specified)

J						
			G (	(	G (	
Thermal resistance, junction to case	per Thyristor	$R_{\text{th(j-c)}}$		0.45		/W
Thermal resistance, junction to Heatsink	per module	$R_{\text{th(j-s)}}$		0.55		/W
Mounting torque	Module torque to heatsink	M	1.8		2.0	N m

I C

	G	J		C	J
JMT135KQ16S2C	JMT135KQ16S2C	S2	16.8g/PCS	90 PCS	810 PCS

FIG.1: On-state characteristics(per thyristor)

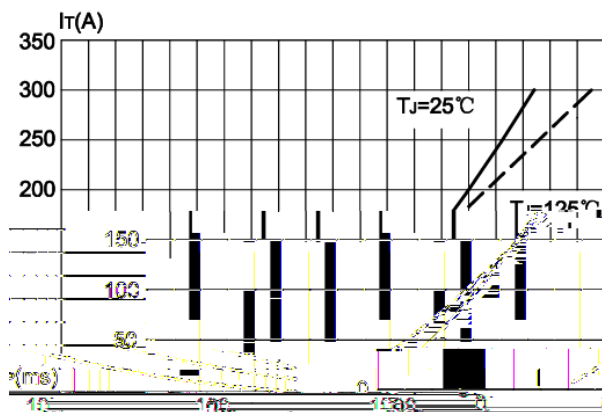
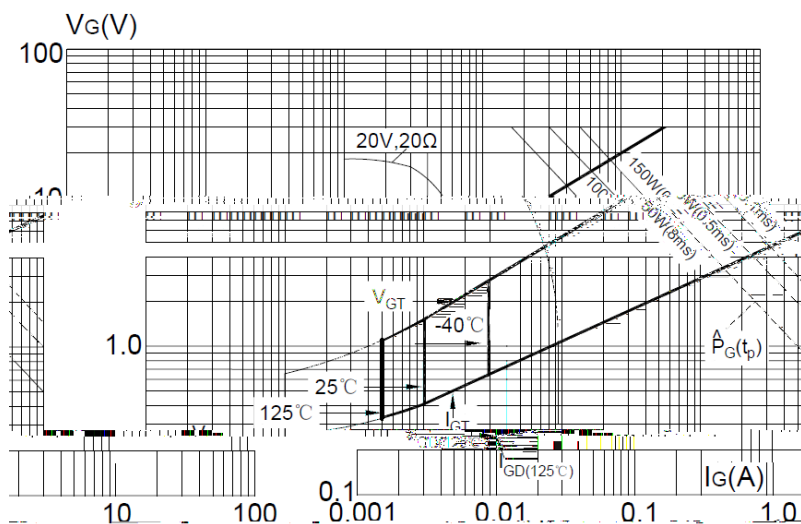
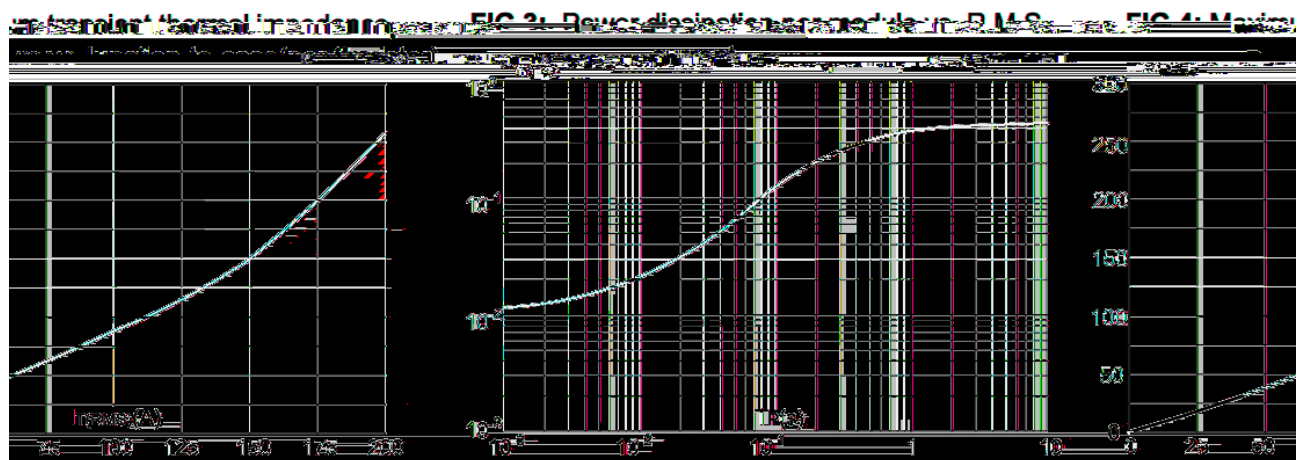
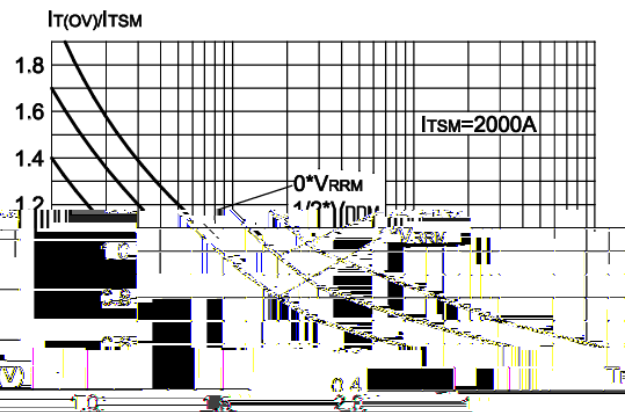
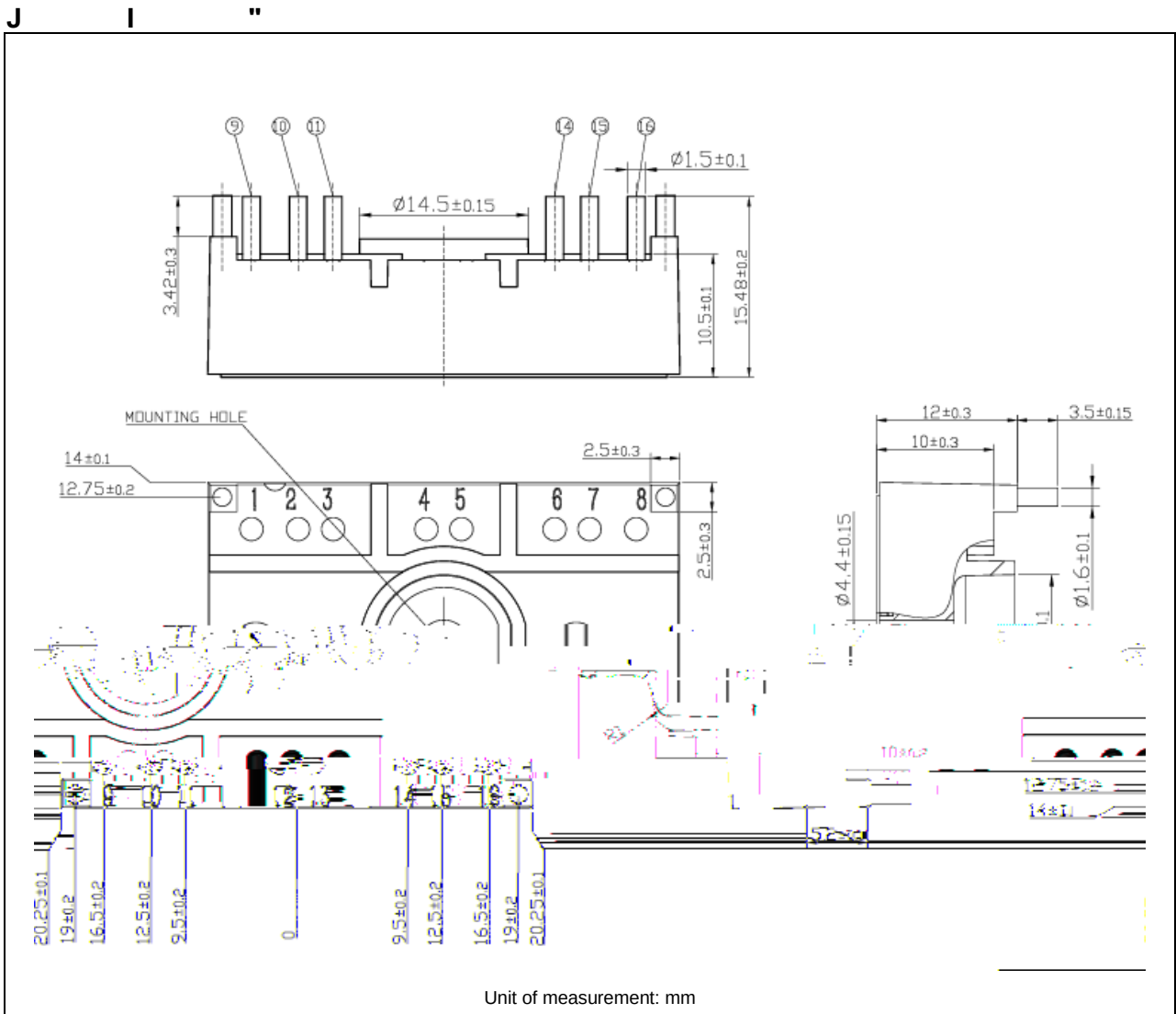
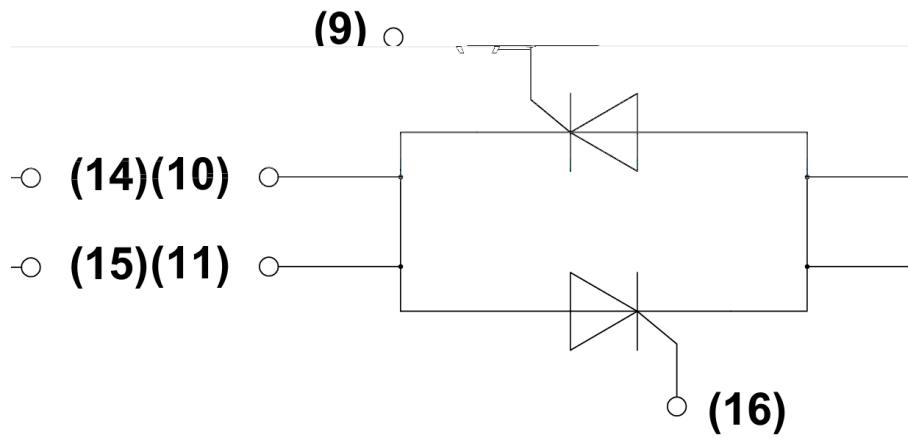


FIG.2: Surge overload current vs. time





**DG****E**

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