



	°C			





			±		

FIG 4: Power dissipation vs. on state current

(per thyristor or diode)

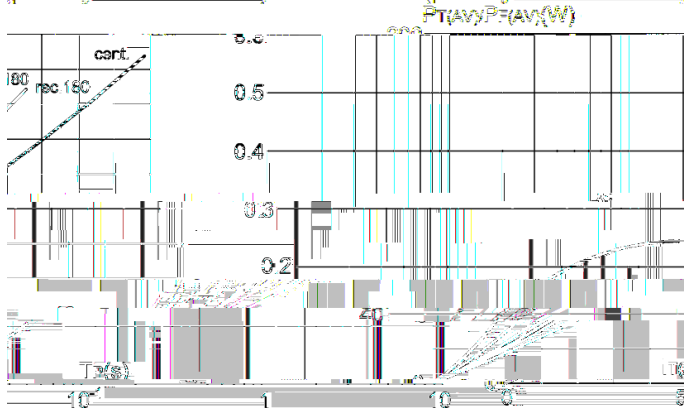


FIG 5: Maximum transient thermal impedance

junction to case/per thyristor or diode

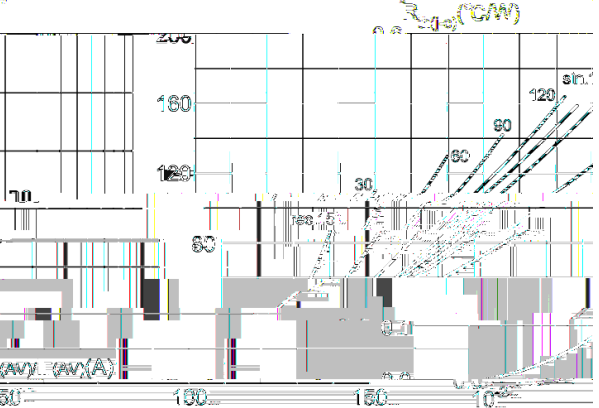


FIG 6: Gate trigger characteristics

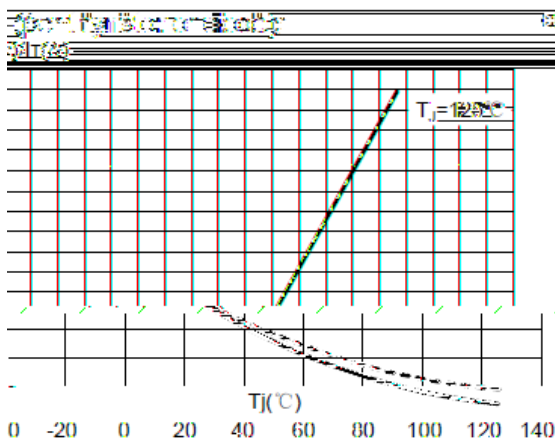


FIG 7: Relative variations of gate trigger current hold

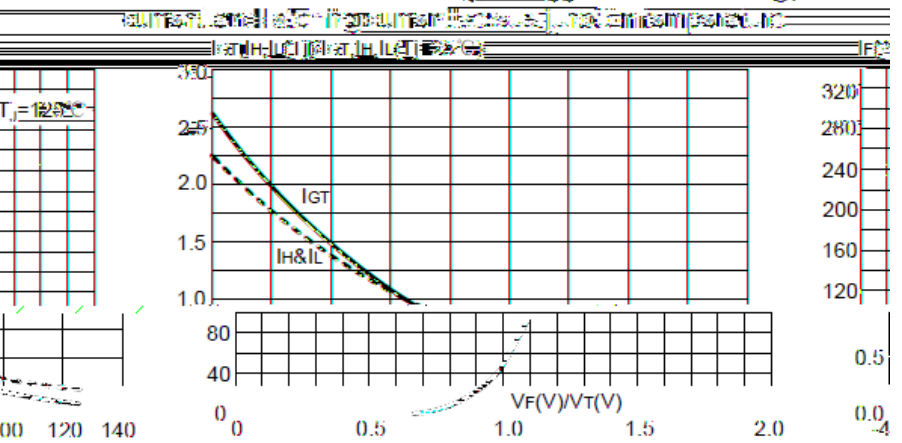


FIG 8: Forward current vs. case temperature

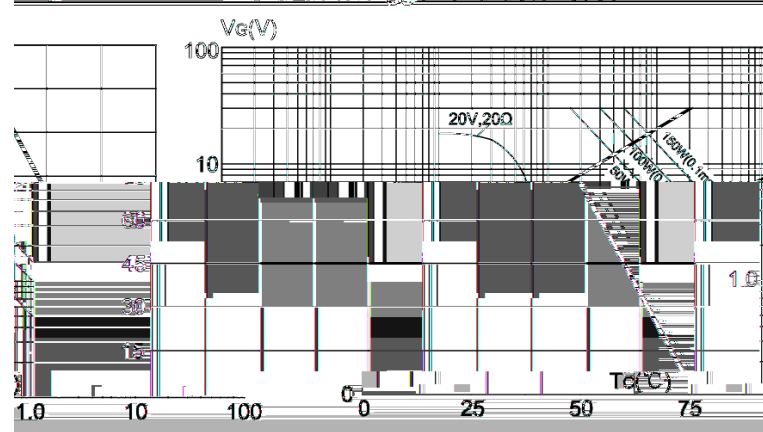


FIG 9: Forward current vs. case temperature

